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REMARKS

Applicants appreciate the Examiners attention to this application. Claims 30-31 and claim 33 have been cancelled, while the other claims of 1-37 remain pending.

Claim Rejections -35 USC § 103(a)

The Office Action states:

3.   Claims 4 and 5 rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The use of "the control logic" qualified with the functionality of the logic to describe multiple logics is extremely confusing. Additionally, both claims lack antecedent basis for the second instance of control logic. Examiner suggests changing each instance of control logic to "a first control logic", "a second control logic", etc... This problem is compounded in claims such as claim 6 where "the control logic" is used alone to reference control logic.

Applicant has amended claims 4 and 5 to make clear that new, different control logic are not being described. Instead, claims 4 and 5 are describing limitation embodiments of pushing the requested data to the first private data cache. Specifically, claim 4 describes one embodiment of pushing including broadcasting, while claim 5 describes another embodiment of unicasting. Hopefully, this amendment makes that purpose more clear and less confusing.

Claim Rejections -35 USC § 103(a)

The Office Action states:

5.   Claims 1, 3-9, 11, 12, 18-21, 23, 25-28, 32 and 34-37 are rejected under 35 U.S.C. 103(a) as being unpatentable over Purser et al. (A Study of Slipstream Processors) herein referred to as Purser, in view Solihin et al. (Prefetching in an Intelligent Memory Architecture Using a Helper Thread) herein referred to as Solihin.

Applicant has amended independent claim 1 to include, " in response to execution of a trigger instruction from the main thread." As inferred in the Office Action through the allowable claims that were objected to (10, 17), neither Purser or Solihin disclose executing the helper (second) thread in response to a trigger instruction in the main thread. Additionally, the novel portions of claims 10 and 17 have been incorporate into independent claims 7 and 11, respectively. Please also note that similar amendments were made to independent claims 18, 25, and 32.

Conclusion

Therefore, applicant respectfully submits that applicant's independent claims 1, 7, 11, 18, 25, and 32, as well as their dependent claims, are now in condition for allowance for at least the reasons stated above. If there are any additional charges, please charge Deposit Account No. 50-0221. If a telephone interview would in any way expedite the prosecution of the present application, the Examiner is invited to contact David P. McAbee at (503) 712-4988.

Respectfully submitted,
Intel Corporation

Dated: April 30, 2010   /David P. McAbee/Reg. No. 58,104
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